

Amendments to the Specification

The paragraph starting at page 1, line 5 and ending at line 8 has been amended as follows.

The present invention relates to an image (or video) signal processing apparatus for image display which is suitable for an image pickup apparatus such as a digital ~~camera~~ camera or the like.

The paragraph starting at page 15, line 16 and ending at page 16, line 6 has been amended as follows.

In the present embodiment, if it is assumed that the image pickup element 10 corresponds to 2,000,000 pixels, a quantity of output data of a screen of the image pickup signal processing circuit 14 corresponds to 1600 horizontal ~~1600~~ pixels and 1200 vertical ~~1200~~ pixels. The resizing circuit 20 resizes the output data of the image pickup signal processing circuit 14 into the data suitable for display and stores it in the VRAM of the memory 24. For example, in a case where the display area of the LCD panel is 521 horizontal (H) ~~521~~ dots and 218 vertical (V) ~~218~~ dots, the size of the VRAM is set to be suitable for the size of this display area, and the output screen size by the resizing circuit 20 is set to be 521 horizontal ~~521~~ pixels and 218 vertical ~~218~~ lines. When an LCD panel of which the number of dots is different from the above is used, the output size setting of the resizing circuit 20 is changed according to this number.

The paragraph starting at page 23, line 17 and ending at page 24, line 1 has been amended as follows.

Concretely, in Fig. 6, the data "00h" is repeated two times (second and third words) after the data "FFh" (first word), and such ~~the~~ data structure is used as the marker. In next data (fourth word), there are bits to judge whether the vertical blanking or the horizontal blanking is designated. When $V = 0$, this data structure corresponds to the RGB data, and when $V = 1$, this data structure corresponds to the vertical blanking. Further, $H = 1$ indicates the start of the RGB data, and $H = 1$ indicates the end of the RGB data. In the horizontal blanking interval, the parts other than the parts E_RGB and S_RGB are filled with data "03H".

The paragraph starting at page 62, line 18 and ending at page 63, line 6 has been amended as follows.

In Fig. 21, a signal BLK is a blanking signal which is transferred from the display sync signal generation circuit 552 to the LCD_CLK generation circuit 530. The LCD_CLK generation circuit 530 uses the ~~simat~~ signal BLK as a sync signal to adjust generation timing of the clock LCD_CLK. When the signal BLK is H, the clock LCD_CLK is given as 7/18 thinned-out clock TV_CLK. When the signal BLK is L, the LCD_CLK generation circuit 530 generates the clock LCD_CLK in a programmable manner to supply necessary clocks in the blanking interval. The signal BLK is acceptable

with the blanking signal of the FIFO memory 525 even if common. However, in this case, in consideration of the delay from the FIFO memory 525 to the dot sequential converter 532, it is necessary for the LCD_CLK generation circuit 530 to give sync timing.

The paragraph starting at page 85, line 20 and ending at line 23 has been amended as follows.

In this case, the program codes themselves read from the storage medium achieve the functions of the above embodiments, and the storage medium storing such the program codes ~~constitute~~ constitutes the present invention.

The paragraph starting at page 86, line 20 and ending at line 22 has been amended as follows.

Further, the present invention is applicable to a system composed of plural ~~equipments~~ pieces of equipment or to an apparatus including a single piece of equipment.

The paragraph starting at page 86, line 23 and ending at page 87, line 4 has been amended as follows.

Further, it is needless to say that the present invention is applicable to a case where a program is supplied to a system or an apparatus to achieve the functions of the

above embodiments. In this case, if a storage medium storing the program represented by software for achieving the present invention is read by the system or the apparatus, such ~~the~~ system or ~~the~~ apparatus can derive the effect of the present invention.

The paragraph starting at page 87, line 5 and ending at line 9 has been amended as follows.

Further, if the program represented by software for achieving the present invention is downloaded and read from a database on a network by a communication program, such ~~the~~ system or ~~the~~ apparatus can derive the effect of the present invention.

The paragraph starting at page 87, line 10 and ending at line 15 has been amended as follows.

As many apparently and widely different embodiments can be made without departing from the spirit and scope of the present invention, it is to be understood that the present invention is not limited to the above embodiments ~~expect~~ except as defined in the appended claims.